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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

S. MIYAZAKI, et al.

Application No.:

10/505,216

Filed:

August 20, 2004

For:

SEMICONDUCTOR INTEGRATED CIRCUIT

Art Unit:

2827

Examiner:

T. LE

<u>AMENDMENT</u>

MS: AMENDMENTS
Commissioner for Patent
P.O. BOX 1450
Alexandria, VA 22313-1450

March 20, 2006

Sir:

In response to the Office Action mailed December 19, 2005, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Claims and

Remarks are included following the amendment.